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TESSERA 3.3-018  
GROUP 2100

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of :  
Khandros et al :  
Application No.: 08/030,194 : Group Art Unit: 2104  
Filed: April 28, 1993 : Examiner: D. Russele  
For: SEMICONDUCTOR CHIP ASSEMBLIES, : Date: July 18, 1994  
METHODS OF MAKING SAME AND :  
COMPONENTS FOR SAME :  
X

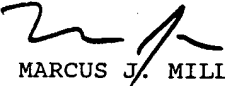
Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

CERTIFICATION PURSUANT TO 37 C.F.R. § 1.97(e)(2)

Sir:

Pursuant to 37 C.F.R. § 1.97(e)(2), undersigned counsel hereby certifies that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application to the above-referenced patent application. Moreover, to the knowledge of undersigned counsel, after making reasonable inquiry, no such item of information was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Commissioner of Patents and Trademarks, Washington, D.C. 20231 on July 18, 1994

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